

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,449,967 B2  
APPLICATION NO. : 10/532993  
DATED : November 11, 2008  
INVENTOR(S) : Michihito Ueda

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page of the patent, in Item “(56) References Cited”, below the data listed under “FOREIGN PATENT DOCUMENTS” and before the Examiner Information insert the following:

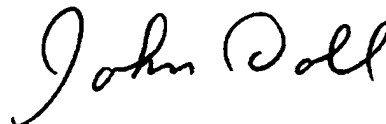
-- OTHER PUBLICATIONS

“YAMANAKA et al.; “A CMOS Stochastic Associative Processor Using PWM Chaotic Signals”; IEICE Trans. Electron.; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729” --

In the claims, in Column 24, Line 4 (Claim 1), change “generator. the” to -- generator, the --.

Signed and Sealed this

Fourteenth Day of April, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*